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(54) Title: PLANARIZATION COMPOSITION FOR REMOVING METAL FILMS (57) Abstract A planarization composition is set forth in accordance with an embodiment of the invention. The composition comprises spherical silica particles having a weight average particle diameter which falls within the range from about 0.03 μ to about 2 μ and is mono-disperse in that at least about 90 weight percent of the particles have a variation in particle diameter from the average particle diameter of no more than about ± 20 %. A liquid carrier comprising up to 20 weight percent ROH, and an amine hydroxide which is NR_4OH or $\text{NR}_2\text{NR}_3\text{OH}$, where each R is HCH_3 , CH_2CH_3 , C_3H_7 OR C_4H_9 , in the amount of 0.1 to 10 weight percent; an oxidizer which is in the amount from about 0.5 to 15 weight percent; an acid stabilizer for adjusting the pH to fall within a range from about 7.0 to about 0.5; and the remainder is water. The invention also relates to a thinning, polishing and planarizing apparatus and to a method for carrying out the thinning, polishing and planarizing operation.		

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PLANARIZATION COMPOSITION
FOR REMOVING METAL FILMS

5 Technical Field

The present invention relates to a planarization composition which is particularly useful for thinning, polishing and planarizing integrated circuitry deposited on semiconductor wafers which have
10 had at least one metal conducting film deposited on their surfaces. The thinning, polishing and planarizing serves to remove the metal film. The invention also relates to a thinning, polishing and planarizing apparatus and to a method for carrying out the thinning, polishing and planarizing operation.

15

Background Of Invention

In semiconductor processing, it is common to deposit a hard material, for example a metal or metal nitride such as tungsten, titanium, titanium/tungsten or titanium nitride, with the deposited
20 material filling holes which extend into the body of an insulating thin film and provide an electrical conducting path from the semiconducting or conducting layer under the insulating surface or conductor to a distance above the surface. The deposited material constitutes what
25 is commonly referred to as a via, plug, trench or contact. When this process is carried out, a thin coating of the material being deposited to form the via is also deposited globally on the upper surface of the insulating layer. This deposited metal layer above the insulating layer must be removed prior to carrying out additional operations on the
30 wafer. The removal of layers of this nature is generally carried out by

a thinning, polishing and planarizing operation utilizing a hard surface of a polishing pad and a polishing slurry which wets the pad and is frictionally moved against the excess deposited metal surface to be removed. The slurry generally includes alumina particles as the
5 abrasive material along with a liquid chemical carrier and reactant. Basically, the alumina and the chemical carrier are respectively hard and reactive enough so as to abrade away the excess deposited metal which has been deposited on the insulating surface when forming the vias, plugs or contact regions. The result, however, is a thinned and
10 scratched surface and is not uniform (planar) enough to be suitable for the manufacture of more advanced integrated circuits which utilize submicron geometries. Further, the thinning, polishing and planarizing operation is considered too slow because of the titanium layer being too resistant to today's thinning, polishing and planarizing operation.

15 It is well known to make use of chemical mechanical polishing (CMP). In this process, more rapid thinning, polishing and planarization can occur through utilization of an abrasive material, generally alumina or silica, along with a liquid carrier and a compound
20 which is corrosive or oxidative toward or will dissolve the substrate. For example, U.S. Patent No. 5,391,258 of Brancaloni, et al. discusses such a process for enhancing the polishing rate of silicon, silica or silicon-containing articles including composites of metals and silica. The composition includes an oxidizing agent along with an
25 anion which suppresses the rate of removal of the relatively soft silica thin film. The suppressing anion may be any of a number of carboxylic acids. Alumina is used as the abrasive material.

It is also known to utilize another abrasive material,
30 specifically silica of very small size, to polish substrates such as rough

cut silicon wafers prior to beginning their processing into integrated circuit devices. As an example, Shimizu, et al., in U.S. Patent 4,842,837, teaches a process for producing fine spherical silica having a particle size of 0.5μ and less. The resulting colloidal silica is used
5 as a polish for semiconductor wafers, specifically silicon wafers. The particles are mono-disperse whereby polishing of the relatively soft silicon wafers surface can be carried out to produce a substantially flat wafer surface. Such particles, by themselves and without the aids of chemical action, are not, however, abrasive enough to be used to
10 effectively polish off the above mentioned materials such as tungsten, titanium or titanium nitride. Indeed, such colloidal silica, which has a pH above 7, and in the absence of an oxidizing agent, is not capable of effectively, i.e., at an acceptably great rate, thinning and planarizing metal or metal nitrides. Furthermore, because the pH is above 7, the
15 exposed silica would be preferentially etched while the metal and metal nitride layers, relatively speaking, remain un-thinned and un-planarized. Substantial and undesirable dishing of the silicon dioxide would occur around the metal and metal nitride plugs, vias, etc. To be an effective thinning and planarizing CMP slurry for metals and metal nitrides, it is
20 essential that the metal and metal nitride be thinned and planarized at a rate substantially higher than the exposed silicon oxides.

PCT Application WO 95/24054, published September 8, 1995, shows that oxidizing agents such as hydrogen peroxide can be
25 added to polishing slurries when the slurries contain an ion for limiting the rate of removal of silicon and silicates as set forth in U.S. Patent No. 5,391,258 discussed immediately above. Other oxidizers are also mentioned. In particular, potassium iodate and sodium iodate are disclosed as being oxidizing agent useful in such polishing slurries. As
30 with U.S. Patent No. 5,391,258 the abrasive agent utilized is alumina.

U.S. Patent No. 5,376,222 discloses the use of colloidal silica in an alkaline solution for polishing a silica film on a semiconductor. The polishing solution includes a cation of an alkali metal, an alkaline earth, or an ammonium ion. The preferred composition includes sodium or potassium ion. The slurry is not used to remove metals but only silica.

U.S. Patent No. 3,877,183 discloses the use of precipitated silicates and/or silicofluorides as polishing substances for semiconductor materials. These polishing substances are utilized to polish the semiconductor, namely, silicon.

A very real problem exists with respect to polishing materials such as metals and metal nitrides and silicon dioxide insulating layers so as to remove them at sufficient rates and selectivities. In particular, when holes are made into insulating layers, for example into doped silicon dioxide layers, and then metals are deposited in those holes to provide conducting metal vias from one level of a semiconductor device to another, the resulting excess metal conducting film on the exposed doped oxide surface of the wafer must be polished away, thinned and planarized without appreciably thinning and planarizing the newly exposed doped or undoped silicon dioxide. It is desirable to stop the thinning and planarizing process once the excess metals and metal nitride have been polished away thus exposing the underlying doped silicon dioxide layer. Ideally, all of the exposed surfaces of the metal, metal nitride and doped silicon dioxide should be perfectly planar. However, the most advanced abrasive and liquid chemical CMP slurries of the prior art are becoming less effective as integrated circuit manufacturers strive for reduced scratch density, improved surface

roughness, greater throughput, improved selectivity, and improved post-CMP surface cleanliness.

At the start of a typical prior art thinning and planarizing (CMP) process, one metal layer is exposed. After a substantial amount of thinning and planarization, a second metal nitride layer, a third metal layer and a silicon dioxide layer are exposed to the thinning and planarizing process. With the conventional alumina-based thinning and planarizing process, which includes, intermixed with the alumina abrasive, an acidic liquid system and an oxidizer, each exposed metal and metal nitride thins and planarizes at a different rate. For instance, the most widely used slurry system thins and planarizes the second exposed metal, the titanium so-called "glue" layer, at a substantially different rate than other exposed metal (tungsten) and metal nitride (titanium nitride) layers. The result is a less planar surface than is desired by the integrated circuit device manufacturer. Further, as the polishing process continues to the titanium layer, the polishing process "stops", or slows down, to a polishing rate 1/6 the rate prior to this metal layer. To date, there has not been a satisfactory solution to this problem.

Today's conventional alumina-based CMP slurries have a particle size distribution exceeding +/- 300 percent. The higher the particle size distribution, the greater the haze, surface roughness and scratches. Integrated circuit manufacturers are finding today that they must reduce particle size distribution because of a future requirement that they use photolithography technology having a shallower depth of focus. Having a requirement for a shallower depth of focus in the future also requires that the surface of the integrated circuit, as it is being manufactured, have less haze, improved surface roughness and

less scratches. Any one of the above parameters, if not improved, can cause an integrated circuit to fail. To date, there has been a substantial effort to reduce the particle size distribution and therefore reduce haze, improve surface roughness and reduce scratch density but without the substantial success required to meet tomorrow's performance requirements (depth of focus) dictated by photolithography.

Furthermore, because of the net positive charge of alumina abrasive particles, when suspended in the liquid chemical, they attach to and therefore "stick" to the newly exposed, negatively charged, doped silica surface thus requiring extensive buff polishing, and mechanical and chemical cleaning procedures to remove alumina slurry remnants that, if not removed, can cause circuitry failure. Accordingly it would be highly desirable, if possible, to develop a polishing composition and polishing method which is easier to clean from the surface after the thinning and planarizing process.

Because of the above shortcomings of today's conventional alumina-based thinning and planarizing CMP slurry, the industry has been looking for an improved thinning and planarizing system which leads to improved planarity, improved thinning and planarizing rate, improved surface haze, roughness and surface scratch density and, finally, an improved CMP composition that leads to increased surface cleanliness after the thinning and planarizing process.

The present invention is directed to overcoming one or more of the problems as set forth above.

Disclosure of Invention

A planarization composition is set forth in accordance with an embodiment of the invention. The composition comprises spherical silica particles having a silica particle concentration of 0.5 to 30 weight percent, a weight average particle diameter which falls within the range from about 0.03 μ to about 2 μ , and is mono-disperse in that at least about 90 weight percent of the particles have a variation in particle diameter from the average particle diameter of no more than about \pm 20%. A liquid carrier comprising up to 20 weight percent ROH, and an amine hydroxide which is NR_4OH or $\text{NR}_2\text{NR}_3\text{OH}$, where each R is any one of H, HCH_3 , CH_2CH_3 , C_3H_7 or C_4H_9 , in the amount of 0.1 to 10 weight percent; an oxidizer which is in the amount from about 0.5 to 15 weight percent; an acid stabilizer for adjusting the pH to fall within a range from about 7.0 to about 0.5; and the remainder is water.

A secondary planarization composition is set forth in accordance with an embodiment of the invention. The composition comprises spherical silica particles having a silica particle concentration of 0.5 to 15 weight percent, a weight average particle diameter which falls within the range from about 0.03 micron to about 2 micron, and is mono-disperse in that at least about 90 weight percent of the particles have a variation in particle diameter from the average particle diameter of no more than about \pm 15%. A liquid carrier comprising up to 45 weight percent ROH, and an amine hydroxide which is NR_4OH or $\text{NR}_2\text{NR}_3\text{OH}$, where each R is any one of H, HCH_3 , CH_2CH_3 , C_3H_7 or C_4H_9 , in the amount of 0.1 to 10 weight percent and the rest is water.

In accordance with another embodiment of the present invention, a pad which is wet with the above planarization composition is set forth.

Yet another embodiment of the invention is a method of planarizing a substrate having a hard metal, metal nitride or insulating surface coating to remove the coating. The method comprises positioning the planarization composition set forth above between a pad and the surface coating and rubbing the pad against the surface coating until the surface coating has been removed.

In accordance with a particularly preferred embodiment of the invention, the materials utilized are all of ultra-pure quality whereby the suspension has a total metals content of no more than about 3,000 parts per billion by weight.

Yet another embodiment of the invention is a method for manufacturing inter-dielectric and inter-metal layer vias and plugs. An insulating layer thin film is deposited onto a surface of a substrate. One or more small holes defined by respective sidewalls and bottoms are etched into the film and extend to at least the substrate surface. A first metal thin film is introduced onto the insulating thin film top surface and onto the respective sidewalls and bottoms to coat the insulating layer thin film and respective sidewalls and bottoms formed, without filling the holes, thereby forming a first metal surface. A first metal nitride thin film is deposited onto the first metal surface in such a manner that it coats the holes and forms a first metal nitride surface. A second metal film is deposited onto the first metal nitride surface in such a manner that it fills the holes and forms a second metal surface. A planarizing composition as set forth above is positioned between the second metal surface and a pad. The pad is rubbed against the second metal surface sufficiently to thin and planarize the first metal thin film, the first metal nitride thin film, and the second metal film.

By making use of amminated, acid-stabilized silica particles which are spherical and monodisperse (of a uniform size and within a specific and limited size range), along with an oxidizer which is useful for oxidizing the hard materials which are to be removed by converting them into materials which can be removed by the relatively soft and uniform silica particles, the ability has been gained to very controllably polish hard materials and to end up with a resultant polished surface which is highly planar. When an oxidizer is also present, as it preferably is, it serves to accelerate the removability of the oxidized compound resulting from reaction of the hard material with the oxidizers. The results obtained have been thinning and planarization rates over twice the conventional rates, one-half the surface roughness levels, substantially reduced wafer non-uniformity, one-tenth the scratch density and a substantial reduction of the post-CMP particle and residue levels encountered after thinning with the conventional alumina-based slurry.

Operating in accordance with the invention, such common via materials as titanium, titanium/tungsten, titanium nitride and tungsten, as well as insulative silicon dioxide, can be removed at controlled rates to provide a planar surface of improved planarity.

Brief Description of the Drawings

25

The invention will be better understood by reference to the figures of the drawings wherein like numbers denote like parts throughout and wherein:

Figure 1 illustrates a schematic view of a pad being wet by the planarization composition of the present invention;

Figure 2 illustrates a pad, wet by the planarization composition in accordance with the present invention, in position to thin, polish and planarize a hard flat surface layer from a partially fabricated integrated circuit;

Figure 3 illustrates the method of planarizing a substrate of the present invention by rubbing of the pad of Figures 1 and 2 across the hard flat surface layer of Figure 2 for providing the desired thinning, polishing and planarizing;

Figure 4 is a cross-sectional view illustrating a sample semiconductor device manufactured using the method for manufacturing inter-dielectric and inter-metal layer vias and plugs of the present invention before the thinning, polishing and planarizing operation; and

Figure 5 illustrates a cross-sectional view of the sample semiconductor device of Figure 4 following a thinning, polishing and planarizing operation to remove the metal films and further illustrates the vias formed into the wafer being processed.

Best Mode for Carrying Out Invention

Figure 1 illustrates a schematic view of a pad 10 being wet with the planarization composition 8 of the present invention. The planarization composition 8 forms a planarization composition thin film 6 on the top surface of the pad 10 to thereby wet the pad 10. A

delivery system 7 brings the planarization composition 8 above the pad 10 which is located on a surface of the polishing platen 60. Another method of wetting the pad 10 includes a holed platen (not shown) having holes (not shown) from the top surface of the platen to the bottom surface of the platen. The planarization composition 8 would then be introduced from the bottom surface of the pad 10 through the bottom surface of the holed platen (not shown) to form a uniform planarization composition thin film. It is understood that either method to wet the pad 10 may be used, or any other method that results in a pad 10 with a uniform planarization composition thin film.

Figure 2 illustrates a schematic view of the pad 10 in accordance with an embodiment of the present invention wet by the planarization composition (not shown) to form the planarization film 6. A quill 40, rotating in a wafer rotating direction 80, holds wafer 30 in the pre-rubbing position 5 to lower the quill 40 and wafer 30 in the rubbing direction 50 towards the pad 10. The pad 10 is supported by polishing platen 60 and rotates in a pad rotating direction 70. The pad 10 is used to thin, polish and planarize the hard flat surface layer 20 of a partially fabricated integrated circuit (not shown) on wafer 30.

Figure 3 illustrates a rubbing position 90 of the quill 40 and wafer 30 downwardly relative to the pre-rubbing position 5 of Fig. 2. In this rubbing position 90, the pad 10 wet with the planarization composition to form the planarization composition thin film 6 rubs against the hard flat surface layer 20 of the wafer 30 to provide the desired thinning, polishing and planarizing of the hard flat surface layer 20. The pad 10 has been wet by a planarization composition with a composition as follows.

In accordance with the present invention, a planarization composition is set forth which comprises from about 0.5 to about 30 weight percent of spherical, mono-dispersed silica particles. The particles have a weight average particle diameter which falls within the range from about 0.03 μ to about 2 μ . More preferably, the weight average particle diameter falls within the range from about 0.02 μ to about 0.4 μ . The particles are mono-disperse, that is, at least about 90 weight percent of the particles have a variation in particle diameter from the weight average particle diameter of no more than about \pm 20%, preferably less than \pm 15%. This is a critical requirement of the particles. The spherical silica particles in accordance with the present invention can be made in the manner discussed and set forth in previous mentioned U.S. Patent No. 4,842,837, the disclosure which is hereby incorporated by reference. The composition further comprises a liquid carrier comprising up to 20 weight percent ROH, and an amine hydroxide which is NR_4OH or $\text{NR}_2\text{NR}_3\text{OH}$, where each R is any one of H, HCH_3 , CH_2CH_3 , C_3H_7 or C_4H_9 , in the amount of 0.1 to 10 weight percent; an oxidizer which is in the amount from about 0.5 to 15 weight percent; an acid stabilizer for adjusting the pH to fall within a range from about 7.0 to about 0.5; and the remainder is water.

Figure 4 illustrates an embodiment of the present invention depicting a cross-sectional view of a sample semiconductor device 100 made using the method for manufacturing an inter-dielectric and inter-metal layer vias and plugs of the present invention. An insulating layer thin film 110 on substrate 120 has been etched to form a hole 130 formed by sidewalls 140 and bottom 150. Etching is performed using any one of many conventional methods. It is understood that while Figure 4 depicts a single hole 130 for a sample semiconductor device 100, the present invention extends to more than one sample

semiconductor device and may have numerous holes. The insulating film 110 is generally a doped or undoped silicon dioxide but may be a polyamide, Teflon, boron nitride, spin-on-glass (SOG), FSG, or N-Parylens. A first metal thin film 160 is deposited on top of the insulating layer thin film 110 and along sidewalls 140 and bottom 150. The first metal thin film 160 merely coats the insulating film 110 and hole 130, sidewalls 140 and bottom 150, rather than filling the entire hole 130. The first metal thin film 160 is preferably titanium, while tungsten or any other metal of similar properties may also be used. The first metal thin film 160 forms a first metal surface 170.

On top of the first metal surface 170 is a first metal nitride thin film 180 that coats the first metal surface 170 and forms a first metal nitride surface 185. A second metal film 190 is then placed on top of the first metal nitride surface 185. The second metal film 190 may be either titanium or tungsten or any other metal having similar properties to these two metals. While the first metal thin film 160 and first metal nitride thin film merely coat the respective surface below each, the second metal film 190 fills the hole 130 and forms a second metal surface 200. It should be understood that while the present embodiment describes a sample semiconductor device 100 with a first metal thin film 160, a first metal nitride film 180 and a second metal film 190, there may be variations in the number of films that still fall within the present invention as claimed below, for example, there may be instances where it is deemed that the first metal 160 is not necessary.

In use, it is desirable to remove by polishing, thinning and planarizing the first metal thin film 160, the first metal nitride thin film 180 and the second metal film 190 to expose only the underlying

insulating layer thin film 110 (generally doped silicon dioxide) as shown in Figure 5. Ideally, all the exposed surfaces of the first metal thin film 160, the first metal nitride film 180 and the second metal film 190 would be perfectly planar at 210. Planarizing is accomplished by
5 positioning the planarization composition described above between the pad 10 against a solid surface coating of the wafer 20 of Fig. 2. In this embodiment, the solid surface coating would include the second metal thin film 190, the first metal nitride thin film 180 and the first metal thin film 160 of Fig. 4, with the second metal surface 200 contacting
10 the pad 10 wet with the aforementioned composition. The pad 10 is rubbed against the second metal surface 200 due to the rotating of the wafer 20 and the pad 10 and the perfectly planar surface 210 of Fig. 5 is accomplished.

15 A secondary planarization process, known as "buff polish", is sometimes necessary after the method of planarizing described above is completed. The secondary planarization process is used when the roughness of the resultant insulating layer thin film 110 of Fig. 5 is not satisfactory or if it is necessary to remove strongly held slurry
20 composition remnants. Certain factors, such as the use of ferric nitrate as an oxidizer in the planarization process above, causes the surface of the exposed insulating thin layer 110 to be too rough and therefore the secondary planarization is needed. The roughness (variance is surface coating) of the insulating film 110 should be less than about 10
25 angstroms, otherwise the secondary planarization process is needed. It should be understood that a secondary planarization process may be needed when additional factors, other than using a ferric nitrate oxidizer in the planarization composition above (see Example 1 below), causes the surface roughness of the insulating layer thin film 110 to be
30 too rough, *i.e.* greater than 10 angstroms.

The secondary planarization process uses a secondary planarization composition comprising spherical silica particles having a silica particle concentration of 0.5 to 15 weight percent, a weight average particle diameter which falls within the range from about 0.03 micron to about 2 micron, and is mono-dispersed in that at least about 90 weight percent of the particles have a variation in particle diameter from the average particle diameter of no more than about $\pm 15\%$. A liquid carrier comprising up to 45 weight percent ROH, and an amine hydroxide which is NR_4OH or $\text{NR}_2\text{NR}_3\text{OH}$, where each R is any one of H, HCH_3 , CH_2CH_3 , C_3H_7 or C_4H_9 , in the amount of 0.1 to 10 weight percent and the rest is water.

The specific method of formulating the silica particles which is currently preferred is to add tetraethylorthosilicate (TEOS) to an ammonium hydroxide solution in water having a pH of approximately 9.0-12.0. The products formed by the reaction include the silica particles of the desired size, ammonia, ammonium ions and ethanol. What results is a suspension of spherical silica particles in an ethanol/ammonium hydroxide solution.

The reaction can be varied somewhat. For example, the TEOS can be replaced with a tetraalkylorthosilicate when the alkyl group comprises methyl, propyl or butyl radicals. If this is done the product is not ethanol but instead the corresponding alcohol. Furthermore, the ammonium hydroxide can be replaced by compounds of the formulas NR_4OH , and/or $\text{NR}_2\text{NR}_3\text{OH}$ where each R is any one of CH_3 , CH_2CH_3 , C_3H_7 or C_4H_9 . The amine hydroxide constituent of the nature defined above will usually be present in the amount from about 0.1% to about 10% weight percent.

Generally, the alcohol constituent will comprise from up to about 20% of the resulting planarization composition, all percents herein being by weight.

5 Any of a number of oxidizers can be included with the liquid carrier. Particularly desirable are such oxidizers as hydrogen peroxide, potassium iodate, ferric nitrate, ferric cyanide, sodium iodate, and peroxyacetic acid. The oxidizer is generally present in an amount from about 0.5% to about 15%, more preferably from about 3% to about
10 13%. The remainder of the composition is water. The total amount of water will not exceed about 70%.

In accordance with the present invention, the pH is adjusted to fall within a range from about 0.5 to about 7, more preferably about
15 0.5 to about 6. The pH is preferably adjusted such that the rate of removal of the oxide formed due to the oxidation reaction between the oxidizer and the metal and metal nitride surface coatings are adjusted to a desired substantially equivalent rate. By a desired equivalent rate is meant a rate such that the coating can be removed in a relatively
20 short time and at a rate substantially equivalent for each metal and metal nitride being removed. Generally, it is preferred that the amount of the oxidizer and the pH are selected such that surface polishing rate of the following materials, which are often used for forming vias, are as follows:

25

Tungsten, 2000 Å to 6,000 Å per minute;
Titanium Nitride, 2000 Å to 6,000 Å per minute; and Titanium,
2000 Å to 6,000 Å per minute. Doped and undoped oxides 50-
200 Å per minute.

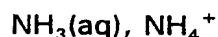
30

Suitably, the pH adjustment will be such as to adjust the pH to be in the acidic range generally in the range from below 7 to about 0.5, preferably from about 4 to about 1. This will provide the necessary acidity. It is not desirable to make the composition too acidic as the
5 vias may then be attacked and the oxidation rate may be too fast.

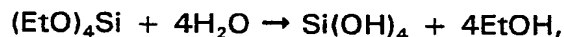
Suitable acids which may be used to adjust the pH include *inter alia*, nitric, sulfuric, hydrochloric, acetic, carboxylic acetic, acetylsalicylic, carboxylic, dicarboxylic acids, and citric acids. There is
10 a concurrent reduction in the amine hydroxide content as the acid is added to the slurry as the amine hydroxide content is reduced as production of a dissolved amine salt of the added acids is increased. The chemistry involved is shown by the following mechanism:

15 I. Hydrolysis of TEOS

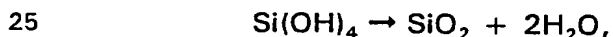
The ammonium hydroxide has five components:



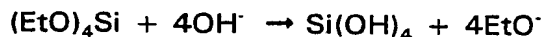
20 These are at least two mechanisms for the hydrolysis of TEOS:



followed by



and



followed by



30 and



At this point, the components in the system are:

H_2O , H_3O^+ , OH^-

5 $\text{NH}_3(\text{aq})$, NH_4^+ ,

EtOH ,

SiO_2 , $(\text{EtO})_4\text{Si}$

II. Addition of Carboxylic Acid

10 The addition of a carboxylic acid, RCO_2H , adds the following additional components:

RCO_2H , RCO_2^- ,

EtOH_2^+

15

III. Addition of Hydrogen Peroxide

The hydrogen peroxide will react with $\text{NH}_3(\text{aq})$ to produce

$\text{NH}_2\text{-NH}_2$, $\text{NH}_2\text{-NH}_3^+$,

20 $\text{NH}_2\text{-OH}$, $\text{NH}_2\text{-OH}_2^+$,

N_2

It will react with EtOH to produce

CH_3CHO ,

25 $\text{CH}_3\text{CO}_2\text{H}$, CH_3CO_2^-

In addition acid-base reactions will generate

H_3O_2^+ , HO_2^- .

IV. Addition of Nitrate Ion

The addition of the nitrate ion can add the following:

5



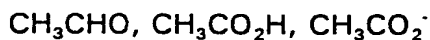
In addition the following nitrogen species can be formed from the oxidation of NH_3 :

10

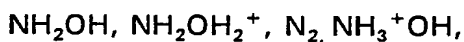
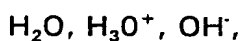


Also, there will be the following oxidation products of EtOH:

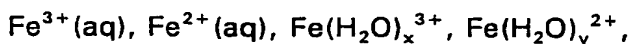
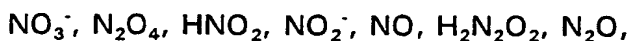
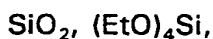
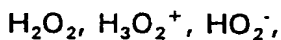
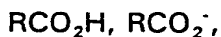
15

V. Summary of Components

20



25

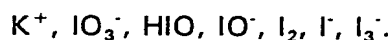


30

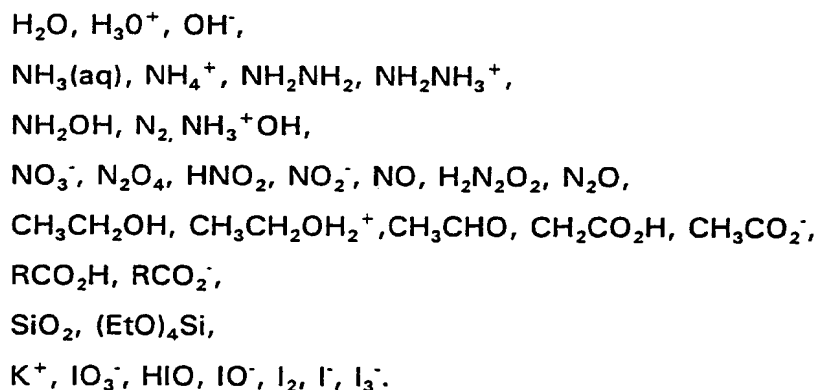


VI. Addition of potassium iodate

With the addition of potassium iodate the following additional
5 species may be present:

VII. Summary of Components

10



20

The temperature for the thinning, polishing and planarizing operation will generally fall within a non-critical range from about 20°C to about 80°C. Indeed, the operation is generally initially at about 20-50°C but the temperature of the surface and the liquid carrier goes up as friction takes place.

25

In accordance with a particularly preferred embodiment of the invention, the materials utilized are all of ultrapure quality whereby the suspension has a total metals content of no more than about 3,000 parts per billion by weight. This is particularly useful in the

semiconductor processing industries since even small amounts of metal can cause errors in devices thereby cutting down on throughput.

The invention will be better understood by reference to the following illustrative experimental examples which demonstrate some of the compositions and advantages of the invention.

Example 1

A planarization composition is formed using an ethyl silicate slurry having a content by weight percent of the following components:

Component	Weight %
Ammonium hydroxide	9.0
ETOH	42.0
SiO ₂	12.0

The pH of the ethyl silicate slurry was approximately 12.0 and the particle size was 188 nm +/- 13.5%. Ferric Nitrate and nitric acid are added to the ethyl silicate slurry in a volumetric ration of 4:1:4. The resultant planarizing composition set forth above is placed between a pad and the surface coating having the following composition:

Surface coatings	Thickness (A)
Tungsten	6,000
Titanium	500
Titanium Nitride	500
Oxide	13,000

The polishing pad was a Thomas West TWI C-22 pad. The example comprises positioning the planarizing composition set forth above between a pad and the surface coating and rubbing the pad against the surface coating according to the following planarizing parameters:

	Parameter	
	Downward Force	7 psi
	Back Pressure	0 psi
10	Table Speed	120 rpm
	Quill Speed	80 rpm
	Planarizing Time	70 seconds

Within a 120 second planarizing period, all surface coatings totaling 7,000 angstroms were cleared to the underlying LPCVD oxide. The measured planarization rate was calculated to be 5,823 angstroms per minute. The tungsten, titanium nitride and titanium filled holes extending into the body of the LPCVD oxide were planar with the LPCVD oxide surface. The selectivity of the tungsten/titanium nitride/titanium surface coating during the planarization process was measured to be 1:1:1.

A secondary planarization process was implemented after the first planarization process. The second planarization process is typically called a "buff polish." A second planarization composition was used which consisted of the same ethyl silicate slurry and ultra-high purity (UHP D.I.) water at a volumetric ration of 1:4, respectively.

A secondary planarization composition is set forth in accordance with an embodiment of the invention. The composition

comprises spherical silica particles having a silica particle concentration of 0.5 to 15 weight percent, a weight average particle diameter which falls within the range from about 0.03 micron to about 2 micron, and is mono-disperse in that at least about 90 weight percent of the particles have a variation in particle diameter from the average particle diameter of no more than about $\pm 15\%$. A liquid carrier comprising up to 45 weight percent ROH, and an amine hydroxide which is NR_4OH or $\text{NR}_2\text{NR}_3\text{OH}$, where each R is any one of H, HCH_3 , CH_2CH_3 , C_3H_7 or C_4H_9 , in the amount of 0.1 to 10 weight percent and the rest is water.

10

The buff polish comprises positioning the second planarizing composition set forth above between a pad and the surface coating and rubbing the pad against the surface coating according to the following planarizing parameters:

15

Parameter

20

Downward Force	7 psi
Back Pressure	0 psi
Table Speed	120 rpm
Table Temperature	50° C
Quill Speed	80 rpm
Planarizing Time	30 seconds

25

The buff polish smooths the oxide surface without planarizing further the planarized tungsten/titanium nitride/titanium holes. The oxide surface roughness improves from 20-30 angstroms Root-Mean-Square ("RMS") to less than ten angstroms. The same Thomas West TWI-C22 pad was used for the buff polish. The planarizing equipment used was a Strasbaugh 6EC single head polisher.

30

Example 2

A planarization composition is formed using an ethyl silicate slurry having a content by weight percent of the following components:

5

Component	Weight %
Ammonium hydroxide	8.5%
ETOH	47.1%
SiO ₂	11.8%

10

The pH of the ethyl silicate slurry was approximately 12.1 and the particle size was 323 +/- 20.0%. Hydrogen Peroxide and Acetic Acid are added to the ethyl silicate slurry in a volumetric ration of 2:1:1. The resultant planarizing composition set forth above

15 between a pad and the surface coating having the following composition:

20

Surface Coatings	Thickness
Tungsten	6,000
Titanium	500
Titanium Nitride	500
Oxide	13,000

25

The polishing pad was a Rodel Suba-500 pad. The example comprises positioning the planarizing composition set forth above between a pad and the surface coating and rubbing the pad against the surface coating according to the following planarizing parameters:

Parameter

	Downward Force	9 psi
5	Back Pressure	0 psi
	Table Speed	120 rpm
	Quill Speed	120 rpm
	Planarizing Time	175 seconds

10 Within a 120 second planarizing period, all surface coatings totaling 7,000 angstroms were cleared to the underlying LPCVD oxide. The measured planarizing rate was calculated to be 2,430 angstroms per minute. The within wafer nonuniformity was +/- 3.5%. The tungsten, titanium nitride and titanium filled holes extending into the
15 body of the LPCVD oxide were planar with the LPCVD oxide surface. The selectivity of the tungsten/titanium nitride/titanium surface coating during the planarization process was measured to be 1:1:1.

 A secondary planarization process was not necessary as was
20 in Example 1 because the oxide surface roughness after the primary planarization process was less than 6.0 angstroms RMS.

Example 3

25 A planarization composition is formed using an ethyl silicate slurry having a content by weight percent of the following components:

	Component	Weight %
	Ammonium hydroxide	9.0
	ETOH	42.0
5	SiO ₂	13.1

The pH of the ethyl silicate slurry was approximately 12.1 and the particle size was 188 +/- 13.5%. Hydrogen Peroxide and Acetic Acid are added to the ethyl silicate slurry in a volumetric ratio of 1:1:1. The resultant planarizing composition set forth above was between a pad and the surface coating having the following composition:

	Surface Coatings	Thickness
15	Tungsten	6,000
	Titanium	500
	Titanium Nitride	500
	Oxide	13,000

The polishing pad was a Rodel IC1000-2A pad. The example comprises positioning the planarizing composition set forth above between a pad and the surface coating and rubbing the pad against the surface coating according to the following planarizing parameters:

25	Parameter	
	Downward Force	7 psi
	Back Pressure	0 psi
	Table Speed	120 rpm
	Quill Speed	80 rpm
30	Planarizing Time	120 seconds

The measured planarization rate was calculated to be 3,469 angstroms per minute. The within wafer nonuniformity was 1.0%. The selectivity of the tungsten/titanium nitride/titanium surface coating during the planarization process was measured to be 1:1:1.

A secondary planarization process was not necessary as was in Example 1 because the oxide surface roughness after the primary planarization process was less than 8 angstroms RMS.

Example 4

Ethyl silicate slurry having a content by weight percent of the following components:

15

Component	Weight %
Ammonium hydroxide	6.0
ETOH	38.0
SiO ₂	13.0

20

The pH of the ethyl silicate slurry was approximately 11.5 and the particle size was 188 nm +/-13.5%. Ferric Nitrate and nitric acid are added to the ethyl silicate slurry in a volumetric ratio of 4:1:4. The resultant planarizing composition set forth above is placed between a pad and the surface coating having the following composition:

25

Surface coatings	Thickness (A)
Tungsten	6,000
Titanium nitride	500
Oxide	13,000

30

In this example there was not a titanium layer present. The polishing pad was a Thomas West TWI C-22 pad. The example comprises positioning the planarizing composition set forth above between a pad and the surface coating and rubbing the pad against the surface coating according to the following planarizing parameters:

Parameter	
Downward Force	7 psi
Back pressure	0 psi
Table Speed	120 rpm
Quill speed	80 rpm
Planarizing time	70 seconds

Within a 120 second planarizing period, all surface coatings totaling 7,000 angstroms were cleared to the underlying LPCVD oxide. The measured planarization rate was calculated to be 5,823 angstroms per minute. The tungsten, titanium nitride and titanium filled holes extending into the body of the LPCVD oxide were planar with the LPCVD oxide surface. The selectivity of the tungsten/titanium nitride/titanium surface coating during the planarization process was measured to be 1:1:1.

A secondary planarization process was implemented after the first planarization process. The second planarization process is typically called a "buff polish". A second planarization composition was used which consisted of the same ethyl silicate slurry and UHP D.I. water at a volumetric ratio of 1:4 respectively.

The secondary planarization composition is set forth in accordance with an embodiment of the invention. The composition

comprises spherical silica particles having a silica particle concentration of 0.5 to 15 weight percent, a weight average particle diameter which falls within the range from about 0.03 micron to about 2 micron, and is mono-disperse in that at least about 90 weight percent of the particles have a variation in particle diameter from the average particle diameter of no more than about $\pm 15\%$. A liquid carrier comprising up to 45 weight percent ROH, and an amine hydroxide which is NR_4OH or $\text{NR}_2\text{NR}_3\text{OH}$, where each R is any one of H, HCH_3 , CH_2CH_3 , C_3H_7 or C_4H_9 , in the amount of 0.1 to 10 weight percent and the rest is water.

The buff polish comprises positioning the second planarizing composition set forth above between a pad and the surface coating and rubbing the pad against the surface coating according to the following planarizing parameters:

Parameter

Downward force	7 psi
Back pressure	0 psi
Table speed	120 rpm
Table temperature	50° C
Quill speed	80 rpm
Planarizing time	60 seconds

The buff polish smoothes the oxide surface without planarizing further the planarized tungsten/titanium nitride/titanium holes. The oxide surface roughness improves from 20-30 angstroms RMS to less than seven angstroms. The same Thomas West TWI-C22 pad was used for the buff polish. The planarizing equipment used was a Strasbaugh 6EC single head polisher.

Industrial Applicability

The present invention provides a composition, article of manufacture and method for carrying out CMP operations to remove
5 such materials as titanium, titanium nitride and tungsten from the surfaces of semiconductor wafers following the deposition of such materials into holes to form vias, contacts and plugs from one level of the semiconductor device to another. Advantageously, the method of the present invention provides a much more uniformly planarized
10 surface than can be obtained by prior art methods. This is extremely important to the semiconductor industry wherein extreme precision on a nanometer scale is essential.

While the invention has been described in connection with
15 specific embodiments thereof, it will be understood that it is capable of further modification, and this application is intended to cover any variations, uses, or adaptations of the invention following, in general, the principles of the invention and including such departures from the present disclosure as come within known or customary practice in the
20 art to which the invention pertains and as may be applied to the essential features hereinbefore set forth, and as fall within the scope of the invention and the limits of the appended claims.

Claims:

I claim:

1. A planarization composition having a pH from about 0.5 to about
5 7, comprising:

from about 0.5 to 30 wt. % of spherical silica particles having a
weight average particle diameter which falls within a range from about
0.03 μ to about 2 μ , and is mono-dispersed in that at least about 90
10 wt. % of the particles having a variation in particle diameter from the
weight average particle diameter of no more than about \pm 20%; and

a liquid carrier comprising the following components in about the
weight percent ranges specified:

15

ROH, up to 20;

an amine hydroxide selected from the group consisting of NR₄OH
and NR₂NR₃OH, wherein each R is any one of H, CH₃, CH₂CH₃, C₃H₇ or
20 C₄H₉, 0.1 to 10;

an oxidizer selected from the group consisting of hydrogen
peroxide, potassium iodate, ferric nitrate, ferric cyanide, sodium iodate,
peroxyacetic acid; and

25

the remainder water.

2. A planarization composition as set forth in claim 1, wherein the pH
is adjusted through addition of an acid selected from the group
30 consisting of nitric, sulfuric, hydrochloric, carboxylic acetic, citric,

acetylsalicylic, carboxylic, and dicarboxylic acids, with concurrent reduction in the amine hydroxide and production of dissolved amine salt.

- 5 3. A planarization composition as set forth in claim 2, wherein the amount of oxidizer and the pH are selected such that surface oxidation of the following listed solids occur within about the rate ranges specified: Tungsten, 2000 Å to 6,000 Å per minute; Titanium Nitride, 2000 Å to 6,000 Å per minute; and Titanium, 2000 Å to 6,000 Å per
10 minute.
- 15 4. A planarization composition as set forth in claim 3, wherein the planarization composition has a total dissolved metals content of no more than about 3,000 parts per billion by weight.
- 20 5. A planarization composition as set forth in claim 1, wherein the planarization composition has a total dissolved metals content of no more than about 3,000 parts per billion by weight.
- 25 6. A planarization apparatus, comprising a pad adapted to be frictionally moved across a surface, the pad having between it and the surface a planarization composition as set forth in claim 1.
- 30 7. A planarization apparatus, comprising a pad adapted to be frictionally moved across a surface, the pad having between it and the surface a planarization composition as set forth in claim 2.
8. A planarization apparatus, comprising a pad adapted to be frictionally moved across a surface, the pad having between it and the surface a planarization composition as set forth in claim 3.

9. A planarization apparatus, comprising a pad adapted to be frictionally moved across a surface, the pad having between it and the surface a planarization composition as set forth in claim 4.

- 5 10. A method of planarizing a substrate having a solid surface coating to remove the coating, comprising the steps of:

positioning the composition of claim 1 between a pad and the surface coating; and

10

rubbing the pad against the surface coating until the surface coating has been removed.

11. The method of claim 10, further comprising the steps of:

15

positioning a secondary planarization composition between the pad and the surface coating, said secondary planarization composition comprising:

- 20 from about 0.5 to 30 wt. % of spherical silica particles having a weight average particle diameter which falls within a range from about 0.03μ to about 2μ , and is mono-dispersal in that at least about 90 wt.% of the particles having a variation in particle diameter from the weight average particle diameter of no more than $\pm 20\%$; and

25

a liquid carrier comprising the following components in about the weight percent ranges specified:

ROH, up to 45;

30

an amine hydroxide selected from the group consisting of NR_4OH and $\text{NR}_2\text{NR}_3\text{OH}$, wherein each R is any one of H_1CH_3 , CH_2CH_3 , CH_4CH_9 , 0.1 TO 10; and

5 the remainder water; and

rubbing the pad against the surface coating until the roughness of the surface coating has been improved to less than ten angstroms.

10 12. A method of planarizing a substrate having a solid surface coating to remove the coating, comprising the steps of:

positioning the composition of claim 2 between a pad and the surface coating; and

15

rubbing the pad against the surface coating until the surface coating has been removed.

13. The method of claim 12, further comprising the steps of:

20

positioning a secondary planarization composition between the pad and the surface coating, said secondary planarization composition comprising:

25 from about 0.5 to 30 wt. % of spherical silica particles having a weight average particle diameter which falls within a range from about 0.03μ to about 2μ , and is mono-dispersal in that at least about 90 wt.% of the particles having a variation in particle diameter from the weight average particle diameter of no more than $\pm 20\%$; and

30

a liquid carrier comprising the following components in about the weight percent ranges specified:

ROH, up to 45;

5

an amine hydroxide selected from the group consisting of NR_4OH and $\text{NR}_2\text{NR}_3\text{OH}$, wherein each R is any one of H , CH_3 , CH_2CH_3 , CH_4CH_9 , 0.1 TO 10; and

10

the remainder water; and

rubbing the pad against the surface coating until the roughness of the surface coating has been improved to less than ten angstroms.

15

14. A method of planarizing a substrate having a solid surface coating to remove the coating, comprising the steps of:

positioning the composition of claim 3 between a pad and the surface coating; and

20

rubbing the pad against the surface coating until the surface coating has been removed.

15. The method of claim 14, further comprising the steps of:

25

positioning a secondary planarization composition between the pad and the surface coating, said secondary planarization composition comprising:

from about 0.5 to 30 wt. % of spherical silica particles having a weight average particle diameter which falls within a range from about 0.03 μ to about 2 μ , and is mono-dispersal in that at least about 90 wt. % of the particles having a variation in particle diameter from the weight average particle diameter of no more than $\pm 20\%$; and

a liquid carrier comprising the following components in about the weight percent ranges specified:

ROH, up to 45;

an amine hydroxide selected from the group consisting of NR_4OH and $\text{NR}_2\text{NR}_3\text{OH}$, wherein each R is any one of H_1CH_3 , CH_2CH_3 , CH_4CH_3 , 0.1 TO 10; and

the remainder water; and

rubbing the pad against the surface coating until the roughness of the surface coating has been improved to less than ten angstroms.

16. A method of planarizing a substrate having a solid surface coating to remove the coating, comprising the steps of:

positioning the composition of claim 4 between a pad and the surface coating; and

rubbing the pad against the surface coating until the surface coating has been removed.

17. The method of claim 16, further comprising the steps of:

5 positioning a secondary planarization composition between the pad
and the surface coating, said secondary planarization composition
comprising:

10 from about 0.5 to 30 wt. % of spherical silica particles having a
weight average particle diameter which falls within a range from about
0.03 μ to about 2 μ , and is mono-dispersal in that at least about 90
wt.% of the particles having a variation in particle diameter from the
weight average particle diameter of no more than $\pm 20\%$; and

15 a liquid carrier comprising the following components in about the
weight percent ranges specified:

ROH, up to 45;

20 an amine hydroxide selected from the group consisting of NR_4OH
and $\text{NR}_2\text{NR}_3\text{OH}$, wherein each R is any one of H , CH_3 , CH_2CH_3 , CH_4CH_3 ,
0.1 TO 10; and

the remainder water; and

25 rubbing the pad against the surface coating until the roughness of
the surface coating has been improved to less than ten angstroms.

18. A method of planarizing a substrate having a solid surface coating to remove the coating, comprising the steps of:

5 positioning the composition of claim 5 between a pad and the surface coating; and

 rubbing the pad against the surface coating until the surface coating has been removed.

10

19. The method of claim 18, further comprising the steps of:

 positioning a secondary planarization composition between the pad and the surface coating, said secondary planarization composition
15 comprising:

 from about 0.5 to 30 wt. % of spherical silica particles having a weight average particle diameter which falls within a range from about 0.03 μ to about 2 μ , and is mono-dispersal in that at least about 90
20 wt.% of the particles having a variation in particle diameter from the weight average particle diameter of no more than $\pm 20\%$; and

 a liquid carrier comprising the following components in about the weight percent ranges specified:

25

 ROH, up to 45;

 an amine hydroxide selected from the group consisting of NR₄OH and NR₂NR₃OH, wherein each R is any one of H₁CH₃, CH₂CH₃, CH₄CH₉,
30 0.1 TO 10; and

the remainder water; and

rubbing the pad against the surface coating until the roughness of the surface coating has been improved to less than ten angstroms.

5

20. A method for manufacturing inter-dielectric and inter-metal layer vias and plugs, comprising the steps of:

10 depositing an insulating layer thin film having a top surface onto a surface of a substrate;

etching one or more small holes defined by respective sidewalls and bottoms into the film and extending to at least the substrate surface;

15

introducing a first metal thin film onto the insulating thin film top surface and onto the respective sidewalls and bottoms without filling the holes thereby forming a first metal surface;

20 depositing a first metal nitride thin film onto the first metal surface in such a manner that the first metal nitride thin film coats the holes and forms a first metal nitride surface;

25 depositing a second metal film onto the first metal nitride surface in such a manner that the second metal film fills the holes and forms a second metal surface;

positioning the planarizing composition of claim 1 between the third metal surface and a pad; and

30

rubbing the pad against the second metal surface sufficiently to thin and planarize the first metal thin film, the first metal nitride thin film, and the second metal film.

5 21. The method of claim 20, further comprising the steps of:

positioning a secondary planarization composition between the pad and the surface coating, said secondary planarization composition comprising:

10

from about 0.5 to 30 wt. % of spherical silica particles having a weight average particle diameter which falls within a range from about 0.03 μ to about 2 μ , and is mono-dispersal in that at least about 90 wt.% of the particles having a variation in particle diameter from the weight average particle diameter of no more than $\pm 20\%$; and

15

a liquid carrier comprising the following components in about the weight percent ranges specified:

20

ROH, up to 45;

an amine hydroxide selected from the group consisting of NR₄OH and NR₂NR₃OH, wherein each R is any one of H₁CH₃, CH₂CH₃, CH₄CH₃, 0.1 TO 10; and

25

the remainder water; and

rubbing the pad against the surface coating until the roughness of the surface coating has been improved to less than ten angstroms.

30

22. A method for manufacturing inter-dielectric and inter-metal layer vias and plugs, comprising the steps of:

5 depositing an insulating layer thin film having a top surface onto a surface of a substrate;

10 etching one or more small holes defined by respective sidewalls and bottoms into the film and extending to at least the substrate surface;

 introducing a first metal thin film onto the insulating thin film top surface and onto the respective sidewalls and bottoms without filling the holes thereby forming a first metal surface;

15 depositing a first metal nitride thin film onto the first metal surface in such a manner that the first metal nitride thin film coats the holes and forms a first metal nitride surface;

20 depositing a second metal film onto the first metal nitride surface in such a manner that the second metal film fills the holes and forms a second metal surface;

25 positioning the planarizing composition of claim 2 between the third metal surface and a pad; and

 rubbing the pad against the second metal surface sufficiently to thin and planarize the first metal thin film, the first metal nitride thin film, and the second metal film.

23. The method of claim 22, further comprising the steps of:

5 positioning a secondary planarization composition between the pad
and the surface coating, said secondary planarization composition
comprising:

10 from about 0.5 to 30 wt. % of spherical silica particles having a
weight average particle diameter which falls within a range from about
0.03 μ to about 2 μ , and is mono-dispersal in that at least about 90
wt.% of the particles having a variation in particle diameter from the
weight average particle diameter of no more than $\pm 20\%$; and

15 a liquid carrier comprising the following components in about the
weight percent ranges specified:

ROH, up to 45;

20 an amine hydroxide selected from the group consisting of NR_4OH
and $\text{NR}_2\text{NR}_3\text{OH}$, wherein each R is any one of H_1CH_3 , CH_2CH_3 , CH_4CH_3 ,
0.1 TO 10; and

the remainder water; and

25 rubbing the pad against the surface coating until the roughness of
the surface coating has been improved to less than ten angstroms.

24. A method for manufacturing inter-dielectric and inter-metal layer
vias and plugs, comprising the steps of:

5 depositing an insulating layer thin film having a top surface onto
a surface of a substrate;

 etching one or more small holes defined by respective sidewalls
and bottoms into the film and extending to at least the substrate
10 surface;

 introducing a first metal thin film onto the insulating thin film top
surface and onto the respective sidewalls and bottoms without filling
the holes thereby forming a first metal surface;

15 depositing a first metal nitride thin film onto the first metal surface
in such a manner that the first metal nitride thin film coats the holes
and forms a first metal nitride surface;

20 depositing a second metal film onto the first metal nitride surface
in such a manner that the second metal film fills the holes and forms
a second metal surface;

 positioning the planarizing composition of claim 3 between the
25 third metal surface and a pad; and

 rubbing the pad against the second metal surface sufficiently to
thin and planarize the first metal thin film, the first metal nitride thin
film, and the second metal film.

30

25. The method of claim 24, further comprising the steps of:

positioning a secondary planarization composition between the pad
and the surface coating, said secondary planarization composition
5 comprising:

from about 0.5 to 30 wt. % of spherical silica particles having a
weight average particle diameter which falls within a range from about
0.03 μ to about 2 μ , and is mono-dispersal in that at least about 90
10 wt.% of the particles having a variation in particle diameter from the
weight average particle diameter of no more than $\pm 20\%$; and

a liquid carrier comprising the following components in about the
weight percent ranges specified:

15

ROH, up to 45;

an amine hydroxide selected from the group consisting of NR_4OH
and $\text{NR}_2\text{NR}_3\text{OH}$, wherein each R is any one of H , CH_3 , CH_2CH_3 , CH_4CH_3 ,
20 0.1 TO 10; and

the remainder water; and

rubbing the pad against the surface coating until the roughness of
25 the surface coating has been improved to less than ten angstroms.

26. A secondary planarization composition, comprising:

from about 0.5 to 30 wt. % of spherical silica particles having a weight average particle diameter which falls within a range from about 0.03 μ to about 2 μ , and is mono-dispersal in that at least about 90 wt.% of the particles having a variation in particle diameter from the weight average particle diameter of no more than $\pm 20\%$; and

a liquid carrier comprising the following components in about the weight percent ranges specified:

ROH, up to 45;

an amine hydroxide selected from the group consisting of NR_4OH and $\text{NR}_2\text{NR}_3\text{OH}$, wherein each R is any one of H_1CH_3 , CH_2CH_3 , CH_4CH_3 , 0.1 TO 10; and

the remainder water.

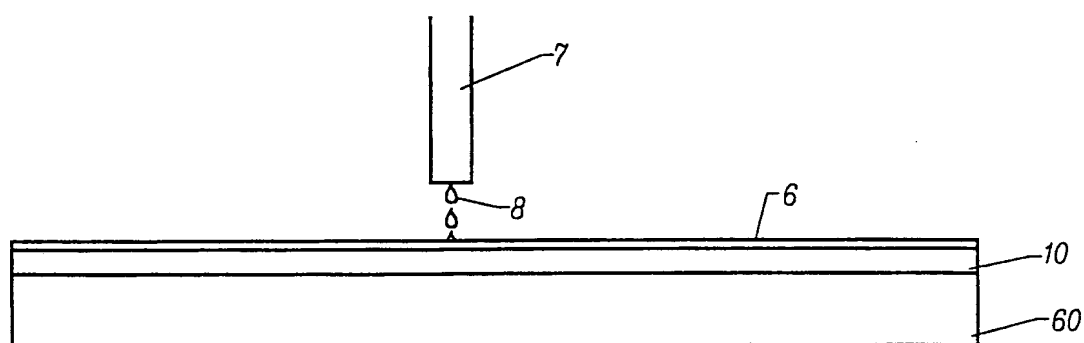


FIG. 1

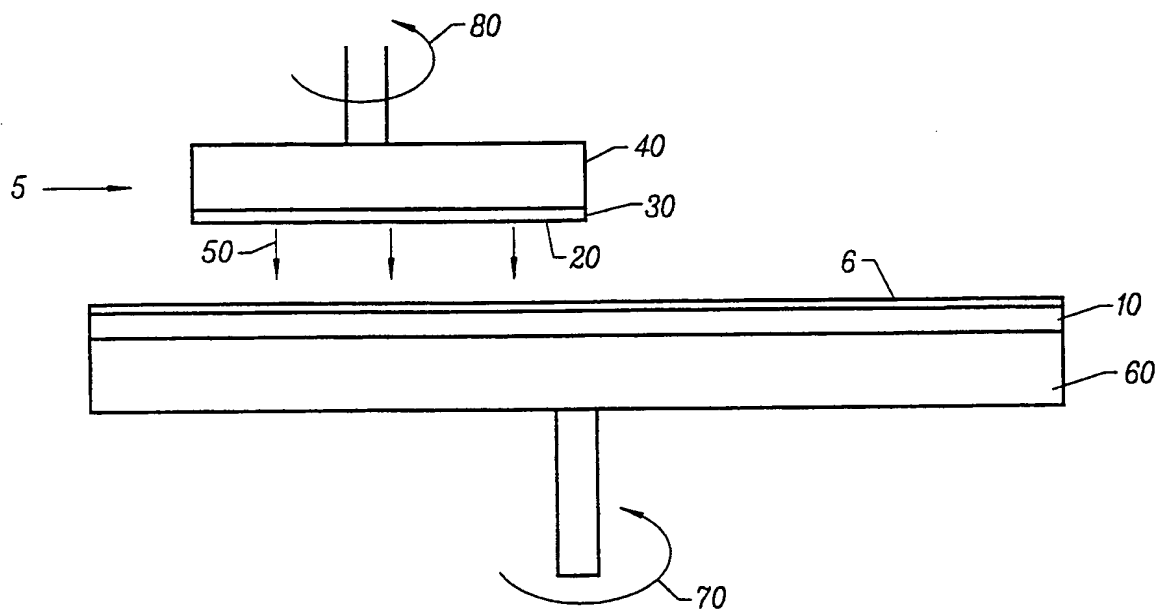


FIG. 2

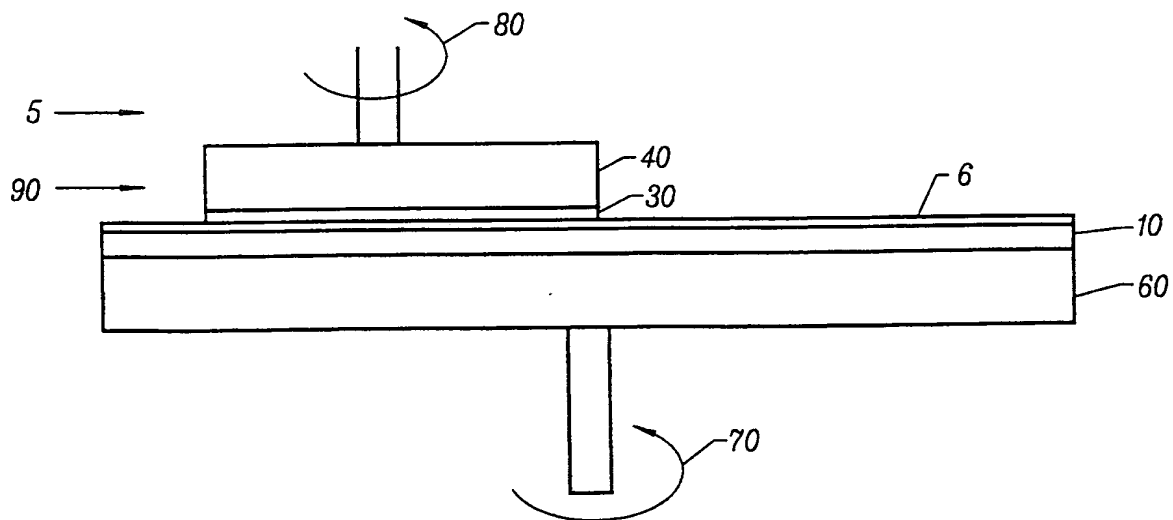


FIG. 3

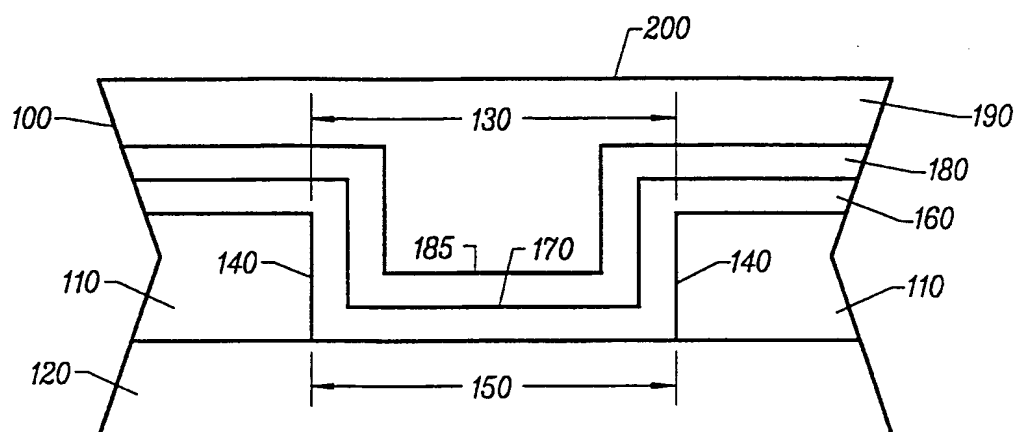


FIG. 4

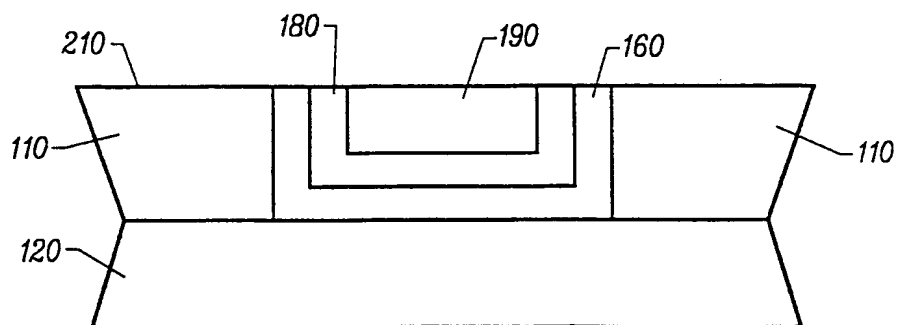


FIG. 5

INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 98/06360

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 C09G1/02 H01L21/321

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 C09G H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	WO 96 16436 A (ADVANCED MICRO DEVICES INC) 30 May 1996 see claims 1,2,5,7 ---	1
A	US 5 340 370 A (CADIEN KENNETH C ET AL) 23 August 1994 see claims 1,2,5 ---	1
A	DATABASE WPI Section Ch, Week 9249 Derwent Publications Ltd., London, GB; Class E16, AN 92-405814 XP002071478 & SU 1 701 759 A (UNIV CHERNOVTSY) see abstract --- -/--	1

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

14 July 1998

Date of mailing of the international search report

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INTERNATIONAL SEARCH REPORT

Intern. Application No

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
P,A	EP 0 826 757 A (SUMITOMO CHEMICAL CO) 4 March 1998 see claims 1,5,9,11 see page 4, line 9 - line 10 -----	1

INTERNATIONAL SEARCH REPORT

Information on patent family members

Interr. Application No
PCT/US 98/06360

Patent document cited in search report		Publication date	Patent family member(s)		Publication date
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EP 0826757	A	04-03-1998	JP	10125638 A	15-05-1998